

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.1280001	APPLICATION NO. 10/083,071
	APPLICANT Gopinathan <i>et al.</i>	
	FILING DATE March 1, 2002	GROUP 2562 2616

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4						
	AB4						
	AC4						
	AD4						
	AE4						
	AF4						
	AG4						
	AH4						
	AI4						
	AJ4						
	AK4						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No
	AP4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
DH	AR	4	Bingham, J.A.C., "Multicarrier Modulation for Data Transmission: An Idea Whose Time Has Come," <i>IEEE Communications Magazine</i> , IEEE, Vol. 28, No. 5, May 1990, pages 5-8 and 11-14.
PH	AS	4	Chiddix, J. <i>et al.</i> , "AM Video on Fiber in CATV Systems: Need and Implementation," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1229-1239.
DH	AT	4	Darcie, T., "Subcarrier Multiplexing for Lightwave Networks and Video Distribution Systems," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1240-1248.

EXAMINER	Duc Ho	DATE CONSIDERED	03-13-07 (12-20-05)
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not

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	FILING DATE March 1, 2002	GROUP 2662 2616

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA9						
	AB9						
	AC9						
	AD9						
	AE9						
	AF9						
	AG9						
	AH9						
	AI9						
	AJ9						
	AK9						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL9						Yes No
	AM9						Yes No
	AN9						Yes No
	AO9						Yes No
	AP9						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
DH	AR	2	Lenosky, T. <i>et al.</i> , "Measurements of DMD-Challenged Fibers at 850nm and 2Gb/s Data Rate," <i>IEEE 802.3ae - Equalization Ad Hoc Group</i> , IEEE, January 10, 2001, 21 pages.
DH	AS	2	Lenosky, T. <i>et al.</i> , "Measurements of DMD-Challenged Fibers at 1310nm and 1Gb/s Data Rate," <i>IEEE 802.3ae - Equalization Ad Hoc Group</i> , IEEE, January 10, 2001, 21 pages.
DH	AT	2	Liu, M-K. and Modestou, P., "Multilevel Signaling and Pulse Shaping for Spectrum Efficiency in Subcarrier Multiplexing Transmission," <i>IEEE Journal of Lightwave Technology</i> , IEEE, Vol. 12, No. 7, July 1994, pages 1239-1246.

EXAMINER PUC tto	DATE CONSIDERED 3-13-07 (12-20-05)
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not

FORM PTO-1449 FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.1280001	APPLICATION NO. 10/085,071
	APPLICANT Gopinathan <i>et al.</i>	
	FILING DATE March 1, 2002	GROUP 2662 2616

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA5						
	AB5						
	AC5						
	AD5						
	AE5						
	AF5						
	AG5						
	AH5						
	AI5						
	AJ5						
	AK5						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
DH	AR	2	Sauer-Greff, W. <i>et al.</i> , "Maximum-Likelihood Sequence Estimation of Nonlinear Channels in High-Speed Optical Fiber Systems," April 6, 2001. Retrieved from the Internet: <URL: http://www.flw.at/Dokumente/010406a.pdf , 29 pages.
DH	AS	2	Williamson, R.C. <i>et al.</i> , "Effects of Crosstalk in Demultiplexers for Photonic Analog-to-Digital Converters," <i>Journal of Lightwave Technology</i> , IEEE, Vol. 19, No. 2, February 2001, pages 230-236.
DH	AT	2	Yang, C-K., "Design Techniques for High-Speed Chip-to-Chip Links," Retrieved from the Internet: <URL: http://web.doc.carleton.ca/courses/97578/topic5/Tutorial_SerialLink.pdf , September 28, 2004, 31 pages.
EXAMINER	DUC H0	DATE CONSIDERED	3-13-07 (12-20-05)

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FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/085,071
				Filing Date	March 1, 2002
				First Named Inventor	Oscar Agazzi
				Art Unit	2616
				Examiner Name	Ho, Duc Chi
Sheet	1	of	1	Attorney Docket Number	1875.1280001
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
DH	NPL1	Izzard, M.J. <i>et al.</i> , "Analog versus Digital Control of a Clock Synchronizer for a 3 GB/s Data with 3.0V Differential ECL," <i>1994 Symposium on VLSI Circuits Digest of Technical Papers</i> , IEEE, pp. 39-40.			
DH	NPL2	Schmidt, L. and Rein, H.-M., "Continuously Variable Gigahertz Phase-Shifter IC Covering more than One Frequency Decade," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 27, No. 6, pp. 854-862, June 1992.			
	NPL3				
	NPL4				
	NPL5				
	NPL6				
	NPL7				
	NPL8				
	NPL9				
	NPL10				

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Examiner Signature	<i>Luchetti</i>	Date Considered	2-26-07
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FORM PTO-1449		Page 1 of 3	
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		PATTY. DOCKET NO. 1875.1280001	APPLICATION NO. 10/085,071
		INVENTORS AGAZZI <i>et al.</i>	ART UNIT 2665
		FILING DATE March 1, 2002	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	5,396,224	03/1995	Dukes <i>et al.</i>			
	AB1	5,550,546	08/1996	Noneman <i>et al.</i>			
	AC1	5,554,945	09/1996	Lee <i>et al.</i>			
	AD1	5,614,855	03/1997	Lee <i>et al.</i>			
	AE1	5,703,905	12/1997	Langberg			
	AF1	5,768,268	06/1998	Kline <i>et al.</i>			
	AG1	5,822,143	10/1998	Cloke <i>et al.</i>			
	AH1	5,881,107	03/1999	Termerinac <i>et al.</i>			
	AI1	5,945,862	08/1999	Donnelly <i>et al.</i>			
	AJ1	5,949,820	09/1999	Shih <i>et al.</i>			
	AK1	5,966,415	10/1999	Bliss <i>et al.</i>			

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	EP 0 515 074	11/1992	EP			N/A
	AM1	EP 0 909 035	04/1999	EP			N/A
	AN1	WO 01/84702	11/2001	WO			N/A
	AO1	WO 01/84724	11/2001	WO			N/A
	AP1	WO 02/071616	09/2002	WO			N/A

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	AR1	Buchwald, A. and Martin, K., <i>Integrated Fiber-Optic Receivers</i> , Kluwer Academic Publishers, ISBN 0-7923-9549-2, Copyright 1995 (entire book submitted).
	AS1	Sidiropoulos, S. and Horowitz, M. A., "A Semidigital Dual Delay-Locked Loop," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 32, No. 11, November 1997, pp. 1683-1692.
	AT1	

EXAMINER	DATE CONSIDERED
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PRIORITY DOCKET NO.

APPLICATION NO.

1875.1280001

10/085,071

THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

INVENTORS

AGAZZI *et al.*

FILING DATE

March 1, 2002

ART UNIT

2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2	6,002,279	12/1999	Evans <i>et al.</i>			
	AB2	6,005,445	12/1999	Katakura			
	AC2	6,009,534	12/1999	Chiu <i>et al.</i>			
	AD2	6,038,269	03/2000	Raghavan			
	AE2	6,122,336	09/2000	Anderson			
	AF2	6,134,268	10/2000	McCoy			
	AG2	6,329,859 B1	12/2001	Wu			
	AH2	6,359,486 B1	03/2002	Chen			
	AI2	6,397,048 B1	05/2002	Toda			
	AJ2	6,404,525 B1	06/2002	Shimomoura <i>et al.</i>			
	AK2	6,498,694 B1	12/2002	Shah			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

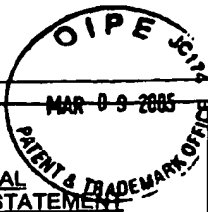
	AR2	
	AS2	
	AT2	

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THIRD SUPPLEMENTAL
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1875.1280001APPLICATION NO.
10/085,071INVENTORS
AGAZZI *et al.*FILING DATE
March 1, 2002ART UNIT
2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DH	AA3	6,509,773 B2	01/2003	Buchwald <i>et al.</i>	—	—	
DH	AB3	6,621,862 B1	09/2003	Dabell	—	—	
DH	AC3	6,791,388 B2	09/2004	Buchwald <i>et al.</i>	—	—	
DH	AD3	2002/0012152 A1	01/2002	Agazzi <i>et al.</i>	—	—	
DH	AE3	2002/0034222 A1	03/2002	Buchwald <i>et al.</i>	—	—	
DH	AF3	2002/0039395 A1	04/2002	Buchwald <i>et al.</i>	—	—	
DH	AG3	2002/0044617 A1	04/2002	Buchwald <i>et al.</i>	—	—	
DH	AH3	2002/0044618 A1	04/2002	Buchwald <i>et al.</i>	—	—	
DH	AI3	2003/0086515 A1	05/2003	Trans <i>et al.</i>	—	—	
DH	AJ3	2004/0212416 A1	10/2004	Buchwald <i>et al.</i>	—	—	
	AK3						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL3						Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	AR3	
	AS3	
	AT3	

EXAMINER

DUC #10

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12-20-05

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